

Diagram illustrating the bit layout for a 64x8 LCD display. The display is organized into a grid of 64 columns and 8 rows. The left side is labeled 'LSB' (Least Significant Bit) at the top and 'MSB' (Most Significant Bit) at the bottom. The right side is labeled 'LCD'. The grid is divided into four 32x4 quadrants by a vertical dashed line and a horizontal dashed line.

MSB

LSB

Sp. a = n

Sp. b = n + 1

Sp. c = n + 2

Sp. d = n + 3

Sp. e = n + 4

8-Bit-Tabelle

Diagram illustrating the bit layout for three rows (n, n+1, n+2) across 12 columns:

- Row n:**
 - Spalte_a Bit 7..0 (bits 0-7)
 - Spalte_b Bit 7..2 (bits 8-11)
- Row n + 1:**
 - Sp.b (bits 0-1)
 - Spalte_c Bit 7..0 (bits 2-9)
 - Salte d (bits 10-11)
- Row n + 2:**
 - Salte d (bits 0-1)
 - Spalte_e Bit 7..0 (bits 2-9)