

$S = \overline{\text{RESET}}$

6 Wilsen

169. \rightarrow

✓

$$D/\bar{C}$$

८६

reset

RA2/AN2/VREF \longleftrightarrow

RA3/AN3/CMP1 \longleftrightarrow RA4/TOCKI/CMP2 \longleftrightarrow

RA5/MCLR/VPP

 $V_{SS} =$

RB0/INT ◀

RB1/RX/DT ◀

RB2/TX/CK ◀

RB3/CCP1 ◀

PIC16F627A/628A/648A

18 RA1/AN1 data

RA0/AN0 *Clock*

RA7/OSC1/CLKIN

RA6/OSC2/CLKOUT

RB7/T10SI/PGD

↔ RB6/T1OSO/T1CKI/PGC

RB5

RB4/PGM

Prig

202736

Doc & Family, Phil


22

AN607

PLA, test

Hand-drawn circuit diagram of a 3.3V regulator. The circuit includes a 9V battery (V_{batt}) connected to the input of an MC33275 regulator. A 10V diode is connected in parallel with the input. A 10 μ F capacitor is connected to the input of the regulator. The output of the regulator is connected to a 10 μ F capacitor, which is labeled V_{out} . The regulator is labeled "3.3V Reg. Adj." and "MC 33275".

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1 Oct 1980

RAY  U_{g2} ~~etc~~ U_{out}
B00410.0.